

Notice of Allowability

Application No.

10/786,276

Examiner

Craig A. Thompson

Applicant(s)

MASLEID ET AL.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to application of 2/24/2004.
2. ☒ The allowed claim(s) is/are 1-24.
3. ☒ The drawings filed on 24 February 2004 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

DETAILED ACTION

Election/Restrictions

Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 1-24, drawn to device, classified in class 257, subclass 296.
- II. Claims 25 and 26, drawn to process, classified in class 438, subclass 197*.

The inventions are distinct, each from the other because of the following reasons:

Inventions II and I are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case the process of group II can be used to make a materially different product including non-SOI devices.

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

During a telephone conversation with Sabra-Anne Truesdale on 11/5/2004 an election was made without traverse to prosecute the invention of group 1, claims 1-24. Claims 25 and 26 are cancelled by Examiner's Amendment below.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Sarra-Anne Truesdale on 11/5/2004.

The application has been amended as follows:

- Claims 25 and 26 are cancelled.

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: prior art of record does not describe or suggest applicants' invention set forth in claims 1-12, an IC ship with redundant elements comprising a substrate die, a plurality of microprocessors, each having a data interface, a plurality of cache memories, each having at least one memory interface, and a signal bus disposed in the die, the bus including a programmable selector circuit for selecting a subset of a plurality of the microprocessors to be used such that the selector circuit simultaneously links the data interface of the first selected microprocessor to a memory interface of a first cached memory and links the data interface of a second selected microprocessor to the memory interface of a second selected cached memory, in the context of the recited device. Similarly, prior art of record does not describe or suggest applicants' invention set forth in claims 13-21, a

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multiprocessor chip with a silicon substrate having at least one SOI region, a plurality of microprocessors formed in the at least one SOI region, a plurality of DRAM cached memories and a high bandwidth signal bus formed on the substrate with interconnect wires for linking the data interface of one microprocessor to a corresponding interface of one of the cached memories, in the context of the recited device. Finally, prior art of record does not describe or suggest applicants' invention set forth in claims 22-24, a multiprocessor chip with a silicon substrate die with at least one bulk region and at least one SOI region, a plurality of microprocessors, a plurality of DRAM level-2 cache memories, and a high bandwidth signal bus for linking the data interface of one microprocessor to a corresponding memory interface of one of the level-2 cache memories as a parallel processor, in the context of the recited device.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Cited Prior Art

Parent Application 09/590,552 has been reviewed in entirety. Prior art provided by the applicant and cited by the examiner in that application has been considered as well and are cited in the attached Notice of References Cited.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Craig A. Thompson whose telephone number is (571)272-1699. The examiner can normally be reached on Monday-Friday 8:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr. can be reached on (571)272-1702. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Craig A. Thompson
Primary Examiner
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12 November 2004